S/N 09/773,502
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Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application.

Claims 68 and 70 are amended.

Claims 73 and 74 are canceled.

Claims 75 and 76 are new.

Listing of Claims:

1-66. (Canceled)

67. (Withdrawn) A semiconductor device having a semiconductor integrated circuit, the semiconductor device comprising:

a plurality of leads for supplying the semiconductor device with a first power source from an outside;

a first internal power source line for supplying an internal circuit of the semiconductor integrated circuit with the first power source; and

a first plurality of internal terminals for supplying the first internal power source line with the first power source from the plurality of leads;

wherein all of the first plurality of internal terminals are connected to the first internal power source line via a first switching portion.

68. (Currently Amended) A semiconductor device having a semiconductor integrated circuit, the semiconductor device comprising:

a first plurality of leads each for supplying the semiconductor device with a first power source from an outside;

a first internal power source line directly connected to an internal circuit including a plurality of logic circuits of the semiconductor integrated circuit and for supplying ~~an~~ the internal circuit of the semiconductor integrated circuit with the first power source; ~~and~~

a first plurality of internal terminals each coupled to a corresponding one of the first plurality of leads and each for supplying the first internal power source line with the first power source from a corresponding one of the first plurality of leads; and

a first switching circuit portion including a first plurality of switching circuits, each of the first plurality of switching circuits interposed between a corresponding one of the first plurality of internal terminals and the first internal source line, where the first plurality of switching circuits includes fewer switching circuits than the first plurality of internal terminals includes internal terminals,

wherein ~~one~~ an internal terminal of the first plurality of internal terminals is connected directly to the first internal power source line~~, and others thereof are connected to the first internal power source line via a first switching portion~~.

69. (Withdrawn) The semiconductor device according to claim 67, comprising:

a second plurality of leads for supplying the semiconductor device with a second power source from the outside;

a second internal power source line for supplying the internal circuit with the second power source; and

a second plurality of internal terminals for supplying the second internal power source line with the second power source from the second plurality of leads;

wherein all of the second plurality of internal terminals are connected to the second internal power source line via a second switching portion.

70. (Currently Amended) The semiconductor device according to claim 68, further comprising:

a second plurality of leads each for supplying the semiconductor device with a second power source having a voltage different from a voltage of the first power source from an outside;

a second internal power source line directly connected to the internal circuit of the semiconductor integrated circuit and for supplying the internal circuit of the semiconductor integrated circuit with the second power source; ~~and~~

a second plurality of internal terminals each coupled to a corresponding one of the second plurality of leads and each for supplying the second internal power source line with the second power source from a corresponding one of the second plurality of leads; and

a second switching circuit portion including a second plurality of switching circuits, each of the second plurality of switching circuits interposed between a corresponding one of the second plurality of internal terminals and the second internal source line, where the second plurality of switching circuits includes fewer switching circuits than the second plurality of internal terminals includes internal terminals.

wherein ~~one~~ an internal terminal of the second plurality of internal terminals is connected directly to the second internal power source line~~, and others thereof are connected to the second internal power source line via a second switching portion~~.

71. (Withdrawn) The semiconductor device according to claim 69, further comprising a third switching portion provided between the first and second internal power source lines.

72. (Withdrawn) The semiconductor device according to claim 69, further comprising a current detecting circuit provided between the first and second internal power source lines.

73-74. (Canceled)

75. (New) A semiconductor integrated circuit, comprising:

an internal circuit including a plurality of logic circuits;

a first internal power source line directly connected to an internal circuit for supplying the internal circuit with the first power source;

a first plurality of internal terminals for supplying the first internal power source line with the first power source; and

a first switching circuit portion including a first plurality of switching circuits, each of the first plurality of switching circuits interposed between a corresponding one of the first plurality of internal terminals and the first internal source line, where the first plurality of switching circuits includes few switching circuits than the first plurality of internal terminals includes internal terminals,

wherein an internal terminal of the first plurality of internal terminals is connected directly to the first internal power source line.

76. (New) The semiconductor device according to claim 75, further comprising:

a second internal power source line directly connected to the internal circuit for supplying the internal circuit with a second power source having a voltage different from a voltage of the first power source;

a second plurality of internal terminals for supplying the second internal power source line with the second power source; and

a second switching circuit portion including a second plurality of switching circuits, each of the second plurality of switching circuits interposed between a corresponding one of the second plurality of internal terminals and the second internal source line, where the second plurality of switching circuits includes fewer switching circuits than the second plurality of internal terminals includes internal terminals,

wherein an internal terminal of the second plurality of internal terminals is connected directly to the second internal power source line.